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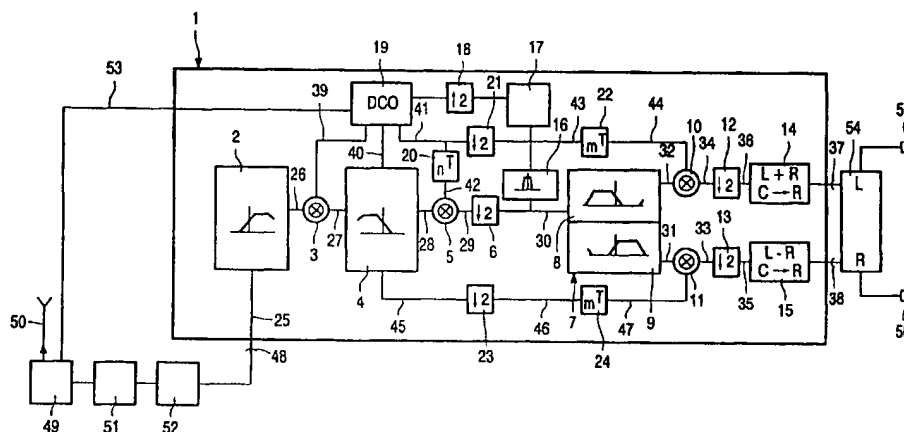
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(54) Title: RECEIVER WITH A SIGNAL PATH



(57) Abstract: The invention relates to a receiver (1, 49, 51, 52, 54) for receiving RF signals. The known receiver comprises a phase-locked loop which is controlled by the stereo pilot. Because of unwanted frequency changes, a sampling rate converter precedes the stereo decoder. Filtering operations within a complex range can be performed by means of the invention.

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# INTERNATIONAL SEARCH REPORT

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<b>A. CLASSIFICATION OF SUBJECT MATTER</b> IPC 7 H04B1/16		
According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) IPC 7 H04B		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, WPI Data		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Y	EP 0 512 606 A (PHILIPS NV) 11 November 1992 (1992-11-11) abstract; figures 1-4	1-10,15
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
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